

	Type	Hits	Search Text
1	BRS	231	(electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)
2	BRS	41	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)
3	BRS	41	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$))
4	BRS	0	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and ((voltaage adj divider) with (\$processor or \$controller))
5	BRS	0	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and ((voltaage adj dividers) with (\$processor or \$controller))
6	BRS	4	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and ((voltage adj divider) with (\$processor or \$controller))
7	BRS	4	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and ((voltage adj divider) with (\$processor or \$controller)))
8	BRS	0	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and (voltage adj divider) near4 tap\$
9	BRS	0	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (voltage adj divid\$)) and ((voltage adj divider) with (current near3 regulat\$))

	Type	Hits	Search Text
10	BRS	0	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (curent near5 (regulat\$ or adjust\$))
11	BRS	30	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (current with characteristic\$)
12	BRS	13	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (current with characteristic\$) and (voltage adj divid\$)
13	BRS	13	((electronic\$ adj commutat\$ adj motor\$) and (\$processor or \$controller)) and (current with characteristic\$) and (voltage adj divid\$)
14	BRS	0	4675583.pn. and (voltage adj divider)
15	BRS	1	4675583.pn. and (\$processor or \$controller)
16	BRS	1	(4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)
17	BRS	0	((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and amplifier and current
18	BRS	1	((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current
19	BRS	1370324	((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and switch\$ or mode\$2
20	BRS	1	((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)
21	BRS	1	((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2))

	Type	Hits	Search Text
22	BRS	1	(((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4
23	BRS	1	(((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4)
24	BRS	1	(((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5
25	BRS	1	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5)
26	BRS	0	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5) and (memory or stor\$5)
27	BRS	0	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5) and nonvolatile
28	BRS	0	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5) and (nonvolatile or non-volatile)
29	BRS	1	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5) and start\$4
30	BRS	1	((((((4675583.pn. and (\$processor or \$controller)) and ((hi or low) with level)) and current) and (switch\$3 or mode\$2)) and referenc\$4) and resist\$5) and start\$4)
31	BRS	3	4675583.pn.
32	BRS	3	4675583.uref.
33	BRS	3	4675583.URPN.

	Type	Hits	Search Text
34	BRS	3	4675583.pn.
35	BRS	3	4675583.uref.
36	IS&R	0	("4675583.uref.").PN.
37	BRS	0	4675583.pn. and fan
38	BRS	0	4675583.pn. and blower
39	BRS	449	motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan
40	BRS	110	(motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan) and (predetermin\$ near6 data)
41	BRS	0	((motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan) and (predetermin\$ near6 data)) and address
42	BRS	88	((motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan) and (predetermin\$ near6 data)) and address
43	BRS	3	((motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan) and (predetermin\$ near6 data)) and address) and (writ\$ near4 protect\$)
44	BRS	3	((motor and (\$processor or \$controller) and (memory with (volatile or nonvolatile)) and interface and fan) and (predetermin\$ near6 data)) and address) and (writ\$ near4 protect\$))
45	BRS	112	700/\$.ccls. and (\$processor near4 (volatile)) and (\$processor near4 (non-volatile or nonvolatile))
46	BRS	3	(700/\$.ccls. and (\$processor near4 (volatile)) and (\$processor near4 (non-volatile or nonvolatile))) and (interface with (motor near6 \$processor))

	Type	Hits	Search Text																																				
47	BRS	3	((700/\$.ccls. and (\$processor near4 (volatile)) and (\$processor near4 (non-volatile or nonvolatile))) and (interface with (motor near6 \$processor)))																																				
48	BRS	89600	motor and (accelerat\$ or (start adj up or start-up))																																				
49	BRS	308	(motor and (accelerat\$ or (start adj up or start-up))) and (\$processor with (volatile and (non-volatile or nonvolatile))) and interface																																				
50	BRS	214	((motor and (accelerat\$ or (start adj up or start-up))) and (\$processor with (volatile and (non-volatile or nonvolatile))) and interface) and (table or directory)																																				
51	BRS	59	((motor and (accelerat\$ or (start adj up or start-up))) and (\$processor with (volatile and (non-volatile or nonvolatile))) and interface) and (table or directory)) and (data adj2 line)																																				
52	BRS	59	((motor and (accelerat\$ or (start adj up or start-up))) and (\$processor with (volatile and (non-volatile or nonvolatile))) and interface) and (table or directory)) and (data adj2 line))																																				
53	BRS	34	<table><tr><td>("4015182"</td><td>"4250544"</td><td>"4346434"</td></tr><tr><td>"4390826"</td><td>"4415844"</td><td>"4459519"</td></tr><tr><td>"4495450"</td><td>"4500821"</td><td>"4540921"</td></tr><tr><td>"4636936"</td><td>"4638233"</td><td>"4642536"</td></tr><tr><td>"4642537"</td><td>"4648551"</td><td>"4653285"</td></tr><tr><td>"4667480"</td><td>"4669040"</td><td>"4688547"</td></tr><tr><td>"4712050"</td><td>"4722018"</td><td>"4736143"</td></tr><tr><td>"4743815"</td><td>"4754405"</td><td>"4860231"</td></tr><tr><td>"4868467"</td><td>"4879502"</td><td>"4902952"</td></tr><tr><td>"4950918"</td><td>"4992715"</td><td>"5019757"</td></tr><tr><td>"5107685"</td><td>"5119071"</td><td>"5170344"</td></tr><tr><td colspan="3">"5202951").PN.</td></tr></table>	("4015182"	"4250544"	"4346434"	"4390826"	"4415844"	"4459519"	"4495450"	"4500821"	"4540921"	"4636936"	"4638233"	"4642536"	"4642537"	"4648551"	"4653285"	"4667480"	"4669040"	"4688547"	"4712050"	"4722018"	"4736143"	"4743815"	"4754405"	"4860231"	"4868467"	"4879502"	"4902952"	"4950918"	"4992715"	"5019757"	"5107685"	"5119071"	"5170344"	"5202951").PN.		
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"5202951").PN.																																							
54	BRS	1	4444390.pn.																																				
55	BRS	1289291	ep 0186850.pn.																																				
56	BRS	9	"5473229"																																				
57	BRS	2	"5744926"																																				
58	BRS	2	5557182.pn.																																				

	Type	Hits	Search Text
59	BRS	137	711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)
60	BRS	137	(711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table))
61	BRS	186746	(711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)) and motor or fan
62	BRS	18	(711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)) and (motor or fan)
63	BRS	18	((711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)) and (motor or fan))
64	BRS	6	711/156.ccls. and microprocessor and (volatile with nonvolatile)
65	BRS	5	(711/156.ccls. and microprocessor and (volatile with nonvolatile)) and interface
66	BRS	0	((711/156.ccls. and microprocessor and (volatile with nonvolatile)) and interface) and memory and directory and object\$
67	BRS	4	((711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)) and (motor or fan)) and memory and directory and object\$
68	BRS	4	((711/156.ccls. and (directory memery or stor\$) and (look-up or lut or lookup or table)) and (motor or fan)) and memory and directory and object\$)
69	BRS	2	5087994.pn.
70	BRS	2	5592058.pn.
71	BRS	0	5592058.pn. and ((table or lut or look-up or lookup) with data)
72	BRS	0	5592058.pn. and ((table or lut or look-up or lookup) with parameter\$)
73	BRS	1	5592058.pn. and (table or lut or look-up or lookup)

	Type	Hits	Search Text
74	BRS	2	5557182.pn.
75	BRS	0	5557182.pn. and ((table or lut or look-up or lookup) with data)
76	BRS	0	5557182.pn. and ram and rom and interface
77	BRS	17963	(volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface
78	BRS	6342	motor and fan and \$processor
79	BRS	1522	(motor and fan and \$processor) and (current near6 control\$)
80	BRS	1522	((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$)
81	BRS	258	((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))
82	BRS	258	((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$)))
83	BRS	0	5592058.pn. and directory
84	BRS	0	5557182.pn. and directory
85	BRS	9	((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and 318/\$.ccls.

	Type	Hits	Search Text
86	BRS	9	((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and 318/\$.ccls.)
87	BRS	9	((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and 318/\$.ccls.) and (start-up or startup or start adj up or accelerat\$)
88	BRS	176	((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and (start adj up or startup or start-up or accelerat\$)
89	BRS	42	((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)
90	BRS	42	((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time))



	Type	Hits	Search Text
91	BRS	0	(((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)) and (motor or fan) and (((start adj up or startup or start-up or accelerat\$) near6 time) with stor\$)
92	BRS	0	(((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)) and (motor or fan) and (((start adj up or startup or start-up or accelerat\$) near6 time) with (memory or lut or table or lookup or look adj up or look-up))
93	BRS	26	(((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)) and (motor near5 current)
94	BRS	18	(((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and (((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)) and (motor near5 current)) and (current with referenc\$)

	Type	Hits	Search Text
95	BRS	18	(((((volatile or ram) and (nonvolatile or rom) and \$processor and (table or lut or lookup or look-up) and interface) and ((motor and fan and \$processor) and (current near6 control\$)) and (current with4 referenc\$))) and (motor or fan) and ((start adj up or startup or start-up or accelerat\$) near6 time)) and (motor near5 current)) and (current with referenc\$))
96	BRS	2	5087994.pn.
97	BRS	391052	5087994.pn. and start\$ or accelerat\$
98	BRS	2	5087994.pn. and (start\$ or accelerat\$)
99	BRS	0	(5087994.pn. and (start\$ or accelerat\$)) and (current with referenc\$)
100	BRS	0	(5592058.pn. and (table or lut or look-up or lookup)) and ((startup or start-up or start adj up or accelerat\$) with time)
101	BRS	0	5592058.pn. and (accelerat\$ with time)
102	BRS	1	5592058.pn. and time
103	BRS	1	5592058.pn. and (time with (start\$ or accelerat\$))
104	BRS	0	5592058.pn. and ((time with (start\$ or accelerat\$)) same (memor\$ or stor\$))

	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1	122	fan and motor and ((star\$5 adj4 current) with speed)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/11/27 15:56
2	BRS	L2	29	1 and (memory or table or lut or stor\$5) and (temperature with (sens\$3 or detect\$3 or monitor\$3))	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/11/27 15:59
3	BRS	L3	10	2 and computer and interface	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2002/11/27 16:00